



DEPARTMENT OF ELECTRONICS AND COMMUNICATION

Report on Faculty Development Program on

"NI LabVIEW Graphical Programming Module-I"

The department of Electronics and Communication Engineering, MVJ College of engineering conducted a Faculty Development program on "NI LabVIEW Graphical Programming Module-I" in association with VI Solutions & National Instruments. This FDP is planned for two weeks i.e., one week is for Non Circuit branches and another week is for Circuit branches. The CLAD trainer from VI Solutions gave the hands on training on LabVIEW for the entire two weeks for both non circuit and circuit branches. The detailed report about the FDP is given as below:

Day 1:

For Non Circuit Branches: 26/12/2016 .

For Circuit Branches: 02/01/2017.

The Session is started on 9.00am in the COE-201. The Vice Principal welcomed the speaker, participants of the FDP and given valuable words about NI LabVIEW Graphical Programming and CLAD Exam.

On forenoon session -1, speaker has explained about Introduction and importance of LabVIEW and the levels of certifications (CLAD, CLD, CLA).

On session-2, speaker explained about the basics of LabVIEW Programming (controls, indicators, data types and functions)

On session -3, Speaker gave the examples on the topics covered in the forenoon session.

Day 2:

For Non Circuit Branches: 27/12/2016

For Circuit Branches: 03/01/2017.

On session -1, speaker has explained about different case structures.

On session -2, speaker has explained about Signal Processing tool box and how to generate different types of signals.

On Session -3, Speaker has explained about the loops and tunneling modes with examples.

Day 3:

For Non Circuit Branches: 28/12/2016

For Circuit Branches: 04/01/2017.

On session – 1, speaker has explained about Arrays and its functions.

On Session – 2 & 3, speaker solved the examples on arrays and its functions.

Day – 4:

For Non Circuit Branches: 29/12/2016 .

For Circuit Branches: 05/01/2017.

On session -1, speaker has explained about Strings and its functions.

On session – 2& 3, speaker solved the examples on strings and its functions

Day – 5:

For Non Circuit Branches: 30/12/2016.

For Circuit Branches: 06/01/2017.

On session - 1, speaker has explained about formula node and expression node with examples.

On session – 2, speaker has explained about debugging tools and shift registers with examples.

On session – 3, speaker has explained about File I/O and Its functions with examples.

Day – 6:

For Non Circuit Branches: 31/12/2016

For Circuit Branches: 07/01/2017.

On session - 1, speaker has explained about Graphs and charts with examples.

On session – 2, speaker has explained about global variables, local variables, state machines and synchronization/ loop communications.



Signature of the Principal with Seal

Principal
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