


01	Name	:	S. VELUSWAMY	
02	Date of Birth	:	05_04_1949	
03	Highest Educational Qualification	:	M.E	
04	Designation	:	HOD, Professor	


Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	B.E.	University of Madras	PSG College of Technology, Coimbatore	Electronics and Communication	1972
b.	M.E.	IISc, Bangalore	IISc, Bangalore	Electrical Communication Engg	1987

05	Work Experience	:	Teaching	Research	Industry	Others	Total
			17.5 yrs	--	35yrs11 mon	--	34,4 yrs
	Date of Joining	:	06_05_2009				

06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:	Digital Communication				
	Practicals	:	Digital Communication				
	Project Work	:	Guided many projects				

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences
	11	24	19	5	40+

08	Projects Carried Out (Nos.)	:	7
09	Patents	:	--
10	Technology Transfer	:	2
11	Research Publications	:	--
12	No. of Books Published with Details	:	--


01	Name	:	Dr. M. Krishnamoorthy	
02	Date of Birth	:	27.04.1943	
03	Highest Educational Qualification	:	M. Sc (Elect), M. S., Ph. D.	
04	Designation	:	Professor	

Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	B. Sc	Madras University	Scott Chri College, Nagercoil	Physics	1963
b.	M. Sc	Madras University	Madura College, Madurai	Electronics	1965
c	M.S	Syracuse University	Syracuse, New York, USA	Bio Physics	1978
d	Ph.D	Madurai Kamaraj	Madurai Kamaraj University	Control System and Network Synthesis (Rheology)	1989

05	Work Experience	:	Teaching	Research	Industry	Others	Total
			39.5	4	-	-	43.5
	Date of Joining	:	01.04.2006				
06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:	Nil				
	Practicals	:	Analog Electronics Lab, Digital Electronics Lab, Power Electronics Lab, Analog Communication Lab, Digital Communication Lab, Microprocessor Lab				
	Project Work	:	Guided and served as a project co-ordinator for 37 projects(2008-09)				

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences
	nil	2	1	1	Nil

08	Projects Carried Out (Nos.)	:	20
09	Patents	:	Nil
10	Technology Transfer	:	Nil
11	Research Publications	:	2
12	No. of Books Published with Details	:	nil

01	Name	:	Dr.Sundarajan	
02	Date of Birth & Age	:	30/11/1945	
03	Highest Educational Qualification	:	Ph.d(Electrical Engineering)	
	Designation	:	Professor	


Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	SSLC	Board of Secondary Education, Madras	Board High School, Dharmapuri	Composite Mathematics	1960
b.	HSC	Madras University	Pachyappa College, Kancheepuram	Science	1961
c.	B. E.	Madras University	GCT, Coimbatore	Electrical Engineering	1968
d.	M. Tech.	IIT, Madras	IIT, Madras	Electrical Engineering	1982
e.	Ph.d	Concordia University, Montreal, Canada	Concordia University, Montreal, Canada	Electrical Engineering	1988

04	Work Experience	:	Teaching	Research	Industry	Others	Total
			13		29		42 years
	Date of Joining & Years	:	03-02-10				
05	Area of Specialization	:	Digital Signal Processing				

06	Subjects taught at Under Graduate Level / Post Graduate Level							
	Under Graduate Level							
	Theory	:	UG	Complex Numbers, Signals and systems, Signal Processing				
			PG	Digital Filters				
	Practical	:	Digital Lab					
Project Work	:	--						

07	Research Guidance Nos.	No. of Papers Published in Master's / Ph.D Level	National Journals	International Journals	Conferences
	--	25	--	25	5(IEEE)

08	Projects Carried Out	:	--
09	Patents	:	3
10	Technology Transfer	:	--
11	Research Publications	:	--
12	No. of Books Published with Details	:	3

01	Name	:	T M Jayanthi	
02	Date of Birth	:	28.06.1972	
03	Highest Educational Qualification	:	ME	
04	Designation	:	Assistant Professor	


Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	B.E.	Kuvempu	AIT, Chicmagalur	Electronics and Communication	1994
b.	M.E.	Bangalore	BMSCE, Bangalore	Digital Communication	2000
c.					

05	Work Experience	:	Teaching	Research	Industry	Others	Total
			11.5				11.5
	Date of Joining	:	06.12.1999				

06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:	Analog Communication, Transmission Lines and Wave Guide, Microprocessors (8085), Advanced Microprocessor (8086), Electronics Circuits, Network Analysis, Optical Fiber Communication, Basic Electronics and Computer Organization, Fundamentals of HDL				
	Practicals	:	Communication Lab, Advanced Communication, Microprocessor Lab, Advanced Microprocessor Lab, Analog Electronics Lab, Digital Electronics Lab, HDL lab, Power Electronics lab				
Project Work	:	Guided around 20 UG projects, 1 PG project					

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences
	nil	nil	nil	nil	5

08	Projects Carried Out (Nos.)	:	2
09	Patents	:	Nil
10	Technology Transfer	:	Nil
11	Research Publications	:	5
12	No. of Books Published with Details	:	Nil

01	Name	:	Padmini M	
02	Date of Birth	:	30.10.1970	
03	Highest Educational Qualification	:	ME (Digital Communication)	
04	Designation	:	Assistant Professor	


Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	B.E.	Bangalore University	Sir M.V.I.T Bangalore	Electronics and Communication	1995
b.	M.E.	Bangalore University	BMSCE Bangalore	Digital Communication	2001
c.					

05	Work Experience	:	Teaching	Research	Industry	Others	Total
			9.5	--	3		12.5
	Date of Joining	:	10.04.2000				

Subjects teaching at Under Graduate Level / Post Graduate Level							
Under Graduate Level							
06	Theory	:	Computer Communication Network, Digital Communication, Digital Signal Processing, Digital Electronics Fundamentals, Linear Integrated Circuits, Logic Design, Electronic Circuits, Telecommunication & Switching, Computer Organization, Basic Electronics, Information Theory and Coding, rDBMS, VLSI P.G. Level Wireless & Mobile communication(MCA),				
	Practicals	:	HDL Communication Lab, Advanced Communication Lab, Microprocessor Lab, Analog Electronics Lab, Digital Electronics Lab, Computer Communication Networks Lab, DSP & Microcontroller Lab				
	Project Work	:	Guided around 20 projects				

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences
	-	-	-	-	-

08	Projects Carried Out (Nos.)	:	2
09	Patents	:	-
10	Technology Transfer	:	-
11	Research Publications	:	-
12	No. of Books Published with Details	:	-


01	Name	:	S.Malini	
02	Date of Birth	:	14-04-1982	
03	Highest Educational Qualification	:	M.E (communication systems)	
04	Designation	:	Senior lecturer	

Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	B.E.	MADURAI KAMRAJ	P.S.N.A COLLEGE of ENGG & TECHNOLOGY.DINDIGUL	ELECTRONICS & COMMUNICATION	2003
b.	M.E.	ANNA UNIVERSITY	KUMARAGURU COLLEGE OF TECHNOLOGY.COIMBATORE	COMMUNICATION SYSTEMS.	2005

05	Work Experience	:	Teaching	Research	Industry	Others	Total
			4.8	-	-	-	4.8yrs
	Date of Joining	:	1-8-2008				
06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:	Transmission lines & Networks, computer networks, digital electronics, professional ethics, Measurements & instrumentation, Transmission lines & wave guides, Antennas & wave propagation, optical communication, wireless networks, electronics circuits, Basic electronics				
	Practicals	:	Electrical & Electronics engg lab, Electronics devices & circuits lab, Microprocessor & micro controller lab, communication lab, electronics circuits & logic design lab				
	Project Work	:	<ol style="list-style-type: none"> 1. Transceiver optimization for multi-user OFDM system in high-speed mobile channels. 2. Watermarking for Audio files. 3. Information hiding using steganography. 				

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences
	nil	3	nil	nil	3

08	Projects Carried Out (Nos.)	:	Nil
09	Patents	:	Nil
10	Technology Transfer	:	Nil
11	Research Publications	:	Nil
12	No. of Books Published with Details	:	Nil


01	Name	:	Shailee Anil Sisodia	
02	Date of Birth	:	07.08.1981	
03	Highest Educational Qualification	:	M. E.	
04	Designation	:	Lecturer	

Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	B.E.	MJP Rohilkhand University, Moradabad	Moradabad Institute of Technology	Electronics and Communication	2003
b.	M.E.	Gujarat University, Ahmedabad	L D College of Engineering	Communication System Engineering	2005

05	Work Experience	:	Teaching	Research	Industry	Others	Total
			4.1				4.1
	Date of Joining	:	06-08-2007				
06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:	Electronic Devices and Circuits – 1, 2, Electronic Communication, Control Engineering, Microwave and Satellite Communication, Analog Communication, Field Theory				
	Practicals	:	Electronic Devices and Circuits Lab – 1, 2, Control engineering Lab, Analog Communication Lab				
	Project Work	:	Cell Search and Maintenance in GSM Network Study project in GPRS Network				

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences
	nil	1	nil	nil	1

08	Projects Carried Out (Nos.)	:	2
09	Patents	:	Nil
10	Technology Transfer	:	Nil
11	Research Publications	:	Nil
12	No. of Books Published with Details	:	Nil

01	Name	:	PRAJOONA VALSALAN	
02	Date of Birth	:	21 12 1980	
03	Highest Educational Qualification	:	M.TECH	
04	Designation	:	LECTURER	


Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	B.E.	KANNUR UNIVERSITY	GOVERNMENT COLLEGE OF ENGINEERING, KANNUR	ECE	2003
b.	M.E.	DEEMED	BHARATH INSTITUTE OF HIGHER EDUCATION & RESEARCH, CHENNAI	VLSI DESIGN	2005
c.	(Ph.D)	KARPAGAM UNIVERSITY	Under the guidance of DR. K.N SHANKARANARAYAN	VLSI DESIGN	

05	Work Experience	:	Teaching	Research	Industry	Others	Total
			3.5 Yrs	-	-	-	3.5 Yrs
	Date of Joining	:	27.01.2009				

06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:	DIGITAL ELECTRONICS, MICRO ELECTRONICS, ELECTRONICS CCTS & DEVICES, BASIC ELECTRONICS				
	Practicals	:	DIGITAL ELECTRONICS, ELECTRONICS CIRCUITS				
	Project Work	:	Microcontroller based speed control for vehicles. Microcontroller based Black box VHDL based Bluetooth security using RSA algorithm.				

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences
					1

08	Projects Carried Out (Nos.)	:	2
09	Patents	:	
10	Technology Transfer	:	
11	Research Publications	:	
12	No. of Books Published with Details	:	


01	Name	:	P.Aparna	
02	Date of Birth	:	20-08-1981	
03	Highest Educational Qualification	:	M.Tech	
04	Designation	:	Lecturer	

Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	B.E.	Jawaharlal Nehru Technological University	Annamacharya Institute of Technology and Sciences, Rajampet, AP	Electronics and Communication	2002
b.	M.E.	JNTU	JNTU College of Engineering, Ananthapur, AP	Digital Electronics and Communication Systems	2008

05	Work Experience	:	Teaching	Research	Industry	Others	Total
			3.5				3.5
	Date of Joining	:	19-03-2008				
06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:	:Electronic Devices and Circuits, Electronic Circuit Analysis, Communication Theory, T V Engineering, Basic Electronics, speech processing, Analog Electronic Circuits, control systems				
	Practicals	:	Electronics Devices and Circuits, Electronics Circuit Analysis, Analog Communication, Analog Electronic Circuits, HDL, Advanced communications.				
Project Work	:	Guided UG projects					

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences
	-	1	-	-	-

08	Projects Carried Out (Nos.)	:	2
09	Patents	:	Nil
10	Technology Transfer	:	Nil
11	Research Publications	:	Application of Discrete Wavelet Transform for Image Compression and Processing, SEC, Vijayawada.
12	No. of Books Published with Details	:	Nil

01	Name	:	K. Praveena	
02	Date of Birth	:	09-06-1984	
03	Highest Educational Qualification	:	M. Tech.	
04	Designation	:	Lecturer	


Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	B.E.	JNTU	Bhoj Reddy Engineering College, Hyderabad	Electronics and Instrumentation	2005
b.	M.E.	JNTU	JNTU College of Engineering, Hyderabad	Geo Informatics	2008

05	Work Experience	:	Teaching	Research	Industry	Others	Total
			2 yrs	--	--	--	2 yrs
	Date of Joining	:	19-03-2008				

06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:	LIC, Optical Fiber communication, Analog & Digital communication, Image processing, Power electronics .				
	Practicals	:	LIC lab, Analog Communication, Advanced communication lab, power electronics lab				
	Project Work	:	Design of L-band cavity backed patch antenna				

07	Research Guidance Nos.	No. of Papers Published in Master's	National Journals	International Journals	Conferences
	-	1	-	-	2

08	Projects Carried Out (Nos.)	:	2
09	Patents	:	Nil
10	Technology Transfer	:	Nil
11	Research Publications	:	Nil
12	No. of Books Published with Details	:	Nil

01	Name	:	V. Nallusamy	
02	Date of Birth	:	19.07.1983	
03	Highest Educational Qualification	:	B. E	
04	Designation	:	Lecturer	


Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	B.E.	Bharathidasan University	Pavendar Bharathidasan College of Engineering & Technology, Trichy, Tamilnadu.	Electronics & Communication Engineering	2004

05	Work Experience	:	Teaching	Research	Industry	Others	Total
			6	Nil	Nil	Nil	6
	Date of Joining	:	16.01.2007				

06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:	Electronic Circuits I & II, Electron Devices & circuits, Microprocessor, Micro controller, Television Engineering, Information Theory and Coding, LIC, VHDL, Entrepreneurship and Management				
	Practicals	:	Electron Devices & circuits Lab, Electronic Circuits II & simulation Lab, Microprocessor and Micro controller lab, Communication lab, VHDL Lab, DSP Lab, DSP and CCN lab				
	Project Work	:	Miniature GPS Microcontroller based timer switching device				

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences
	nil	nil	nil	nil	Nil

08	Projects Carried Out (Nos.)	:	2
09	Patents	:	Nil
10	Technology Transfer	:	Nil
11	Research Publications	:	Nil
12	No. of Books Published with Details	:	Nil

01	Name	:	Shiva Nooka	
02	Date of Birth	:	10-05-1985	
03	Highest Educational Qualification	:	M.Tech	
04	Designation	:	Lecturer	


Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	B.E.	Anna University	Sree Sastha Institute Of Engineering and Technology, Chennai	Electronics and Communication	2006
b.	M.E.	JNTU	Annamacharya Institute of Technology and Sciences, Rajampet, AP	Digital Electronics and Communication Systems	2008

05	Work Experience	:	Teaching	Research	Industry	Others	Total
			2 yrs				2 yrs
	Date of Joining	:	20-10-2008				

06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:	Field Theory, Micro Controllers, Communication Systems				
	Practicals	:	DSP, Controllers, HDL				
	Project Work	:	Embedded Systems, Image Processing				

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences
	nil	nil	nil	nil	Nil

08	Projects Carried Out (Nos.)	:	2
09	Patents	:	Nil
10	Technology Transfer	:	Nil
11	Research Publications	:	Nil
12	No. of Books Published with Details	:	Nil


01	Name	:	Jisha P	
02	Date of Birth	:	17-03-1983	
03	Highest Educational Qualification	:	BE	
04	Designation	:	Lecturer	

Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	B.E.	Mahatma Gandhi University	Marbaselious Christian College of Engineering and Technology, Peermad	Electronics and Communication Engineering	2005

05	Work Experience	:	Teaching	Research	Industry	Others	Total
			4.6yrs				4.6yrs
	Date of Joining	:	11.02.2008, 11 Months				
06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:	Microwave Devices and Communication, Digital Communication, Satellite Communication, Analog Communication, Mobile and Wireless Communication, Basic Electronics, Microprocessors and Micro controllers, Radiation and Propagation, Television and Radar Engineering, Computer Communication, Basic electrical Engineering, Optical Fiber communication				
	Practicals	:	Analog Communication, Digital Communication, Microprocessor, Electronic Devices Lab, Computer Communication Lab				
	Project Work	:	1.Agent One – Remote controlled Car 2.Microcontroller based Smart Room 3.Touch panel based Students information access 4.Design of Industrial Automated Robot for Transportation of Products.				

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences
	nil	nil	nil	nil	Nil

08	Projects Carried Out (Nos.)	:	1
09	Patents	:	Nil
10	Technology Transfer	:	Nil
11	Research Publications	:	Nil
12	No. of Books Published with Details	:	Nil


01	Name	:	S. Nidhya	
02	Date of Birth	:	06.03.1983	
03	Highest Educational Qualification	:	BE	
04	Designation	:	LECTURER	

Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	B.E.	Bharathidasan University, Tiruchirapalli	Pavendar Bharathidasan College of Engineering & Technology, Tiruchirapalli.	Electronics & Communication Engineering	2004
b.	M.E.				
c.					

05	Work Experience	:	Teaching	Research	Industry	Others	Total
			4.5	-	-	-	4.5
	Date of Joining	:	11 th APRIL 2005				
06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:	Logic design, Network Analysis, Information Theory and Coding, Operation Research and Management				
	Practicals	:	Digital lab, Analog lab, Computer Communication Lab, DSP Lab, Microprocessor Lab				
Project Work	:	Telerecontrol system GSM Process Control RFID based Video Surveillance system					

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences
	nil	nil	nil	nil	nil

08	Projects Carried Out (Nos.)	:	Nil
09	Patents	:	Nil
10	Technology Transfer	:	Nil
11	Research Publications	:	Nil
12	No. of Books Published with Details	:	Nil


01	Name	:	M. Suresh Kumar	
02	Date of Birth	:	01.06.1983, 24	
03	Highest Educational Qualification	:	B. E	
04	Designation	:	Lecturer	

Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	B.E.	Anna University	Govt. College of Engineering, Bargur, Tamilnadu.	Electronics & Communication Engineering	2005
b.					
c.					

05	Work Experience	:	Teaching	Research	Industry	Others	Total
			2.6	Nil	Nil	Nil	2.6
	Date of Joining	:	01.03.2007				
06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:	C/C++, RDBMS, Microprocessor, Microcontroller, VHDL				
	Practicals	:	Communication lab, Advanced Microprocessor Lab				
	Project Work	:	Nil				

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences
	nil	In progress	nil	nil	Nil

08	Projects Carried Out (Nos.)	:	4
09	Patents	:	nil
10	Technology Transfer	:	Nil
11	Research Publications	:	Nil
12	No. of Books Published with Details	:	In progress


01	Name	:	Ere Gowda K. T.	
02	Date of Birth	:	01.05.1983	
03	Highest Educational Qualification	:	B. E.	
04	Designation	:	Lecturer	

Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	B.E.	Visvesvaraya Technological University	KNS Institute of Technology	Electronics and Communication Engineering	2007
b.	M.E.				
c.					

05	Work Experience	:	Teaching	Research	Industry	Others	Total
			2.6yrs	--	1	--	3.6 yrs
	Date of Joining	:	08/08/07				
06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:	Operating System, Field Theory, LIC and applications, Embedded System , Programming in C++, Real Tme System				
	Practicals	:	Digital Lab, VHDL Lab, Micro controller Lab, VLSI Lab				
Project Work	:	1. Design & implementation of CAN Protocol in Micro controller system 2. Intelligent wireless emergency exit system with visual support					

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences
	-	-	-	-	-

08	Projects Carried Out (Nos.)	:	1
09	Patents	:	Nil
10	Technology Transfer	:	Nil
11	Research Publications	:	Nil
12	No. of Books Published with Details	:	Nil


01	Name	:	Syeda Nayeemunisa	
02	Date of Birth	:	12.05.1985	
03	Highest Educational Qualification	:	B. E.	
04	Designation	:	Lecturer	

Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	B.E.	Visvesvaraya Technological University	Adichunchanagiri Institute of Technology	Electronics and Communication	2007

05	Work Experience	:	Teaching	Research	Industry	Others	Total
			2.4 years	--	--	--	2.4 years
	Date of Joining	:	23/08/07				
06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:	Logic Design, Embedded Systems, Microwave communication, Digital Communication, , Analog Communication, Microprocessors and Microcontrollers, Television and Radar Engineering, Computer Communication, Basic Electronics Engineering.				
	Practicals	:	Analog Communication, Digital Communication, Microprocessor, Electronic Devices Lab, Computer Communication Lab				
	Project Work	:	1. Microcontroller based GSM Automated Car Parking 2. Home Networking using using Zigbee				

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences
	-	-	-	-	-

08	Projects Carried Out (Nos.)	:	Nil
09	Patents	:	Nil
10	Technology Transfer	:	Nil
11	Research Publications	:	Nil
12	No. of Books Published with Details	:	Nil


01	Name	:	Shwetha M.	
02	Date of Birth	:	29.05.1986	
03	Highest Educational Qualification	:	B. E.	
04	Designation	:	Lecturer	

Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	B.E.	Visvesvaraya Technological University	Adichunchanagiri Institute of Technology	Electronics and Communication	2007

05	Work Experience	:	Teaching	Research	Industry	Others	Total
			2.4 Yrs	-	-	-	2.4 Yrs
	Date of Joining	:	23.08.2007				
06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:	Basic Electronics, Information Theory & Coding, Operating System, Logic Design, Microprocessor (8086), Operation Research And Management, Network Analysis				
	Practicals	:	Microprocessor (8086) Lab, VLSI lab, Analog Communication, Logic Design				
	Project Work	:	<ul style="list-style-type: none"> Guided 2 UG Project 				

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences
	-	--	-	-	1

08	Projects Carried Out (Nos.)	:	Rf Based Automatic Railway Gate Control System (BE Project)
09	Patents	:	Nil
10	Technology Transfer	:	Nil
11	Research Publications	:	Nil
12	No. of Books Published with Details	:	Nil


01	Name	:	T Malvika	
02	Date of Birth	:	24-09-1984	
03	Highest Educational Qualification	:	BE	
04	Designation	:	Lecturer	

Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	B.E.	Anna University	Easwari Engg College	ECE	2005

05	Work Experience	:	Teaching	Research	Industry	Others	Total
			1 yrs	-	-	-	1 yrs
	Date of Joining	:	23-01-09				
06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:	Wireless communication, Network Analysis, Fiber optical communication.				
	Practicals	:	Power Electronics Lab.				
	Project Work	:	Guided 2 UG projects				

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences
	--	--	-	-	--

08	Projects Carried Out (Nos.)	:	1
09	Patents	:	Nil
10	Technology Transfer	:	Nil
11	Research Publications	:	Nil
12	No. of Books Published with Details	:	Nil


01	Name	:	Brindha M	
02	Date of Birth & Age	:	02.07.1983, 26yrs	
03	Highest Educational Qualification	:	M.E	
	Designation	:	LECTURER	

Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	SSLC	Matriculation	Sri Sharada Matriculation School, Tamil nadu	-	1998
b.	12th	State Board	Sarah Tucker Higher Secondary School, Tamil nadu	PCMC	2000
c.	B. E.	M.S University	Govt College of Engg, Tirunelveli, Tamil Nadu	Electronics and Communication Engineering	2004
d.	M.E	Anna University	College of Engg, Chennai, Tamil Nadu	Electrical Drives and Embedded Control	2007

04	Work Experience	:	Teaching	Research	Industry	Others	Total	
			1.4Yr	-	-	-	1.4 Yr	
	Date of Joining & Years	:	02.05.2007, 1 Yr					
05	Area of Specialization	:						
06	Subjects taught at Under Graduate Level / Post Graduate Level							
	Under Graduate Level							
	Theory	:	UG	VLSI, DSP, Fundamentals of HDL				
			PG	-				
	Practical	:	Micro controller & VLSI Lab, HDL Programming Lab					
Project Work	:	--						

07	Research Guidance Nos.	No. of Papers Published in Master's / Ph.D Level	National Journals	International Journals	Conferences
	-	-	-	-	-

08	Projects Carried Out	:	-
09	Patents	:	-
10	Technology Transfer	:	-
11	Research Publications	:	-
12	No. of Books Published with Details	:	-

01	Name	:	K.Anish Pon Yamini	
02	Date of Birth	:	14/01/1982	
03	Highest Educational Qualification	:	M.Tech	
04	Designation	:	Lecturer	


Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	M.Tech	SRM Deemed University	SRM Institute of Science and Technology	Communication Systems	2005
b.	B.E	Manonmanium University	Jayamatha Engineering College	Electronics and Communication Engineering	2003

05	Work Experience	:	Teaching	Research	Industry	Others	Total
			5months				5months
	Date of Joining	:	12/10/2009				

06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:	Basic Electronics				
	Practicals	:	Circuits Lab				
	Project Work	:	--				

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences
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08	Projects Carried Out (Nos.)	:	---
09	Patents	:	---
10	Technology Transfer	:	---
11	Research Publications	:	---
12	No. of Books Published with Details	:	---


01	Name	:	SHANTHI.K	
02	Date of Birth & Age	:	24.03.1983 & 26	
03	Highest Educational Qualification	:	M.E.,	
	Designation	:	LECTURER	

Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	SSLC	STATE BOARD	THE GIRL'S HIGHER SEC SCHOOL SRIRANGAM, T.N	----	1998
b.	HSC	STATE BOARD	THE GIRL'S HIGHER SEC SCHOOL SRIRANGAM, T.N	BIOLOGY	2000
c.	B. E.	BHARATHDASAN UNIVERSITY	M.KUMARASAMY COLLEGE OF ENGG, T.N	E.C.E.,	2004
d.	M. Tech.	ANNA UNIVERSITY	CRESCENT ENGG COLLEGE, CHENNAI,T.N	COMMUNICATION SYSTEMS	2006

04	Work Experience	:	Teaching	Research	Industry	Others	Total	
			2YRS & 4MONTHS				2YRS & 4MONTHS	
	Date of Joining & Years	:	4.3.10					
05	Area of Specialization	:	ELECTRON DEVICES AND CIRCUITS					
06	Subjects taught at Under Graduate Level / Post Graduate Level							
	Under Graduate Level							
	Theory	:	UG	TRANSMISSION LINES & WAVE GUIDES, MICROWAVE ENGG, ELECTRON DEVICES & CIRCUITS				
			PG	---				
	Practical	:	ELECTRON DEVICES AND CIRUITS LAB, DSP LAB					
Project Work	:							

07	Research Guidance Nos.	No. of Papers Published in Master's / Ph.D Level	National Journals	International Journals	Conferences
		1	1		

08	Projects Carried Out	:	
09	Patents	:	
10	Technology Transfer	:	
11	Research Publications	:	.
12	No. of Books Published with Details	:	


01	Name	:	Sreekala K.	
02	Date of Birth & Age	:	15-10-1980/29	
03	Highest Educational Qualification	:	M.E.	
	Designation	:	Lecturer	

Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	SSLC	Kerala Board	Mambaram H. S., Kannur.	10 th std	1996
b.	HSC	Calicut University	Nirmalagiri college, Kannur.	Science group	1998
c.	B. E.	V.T.U.	Vidya Vardhaka College of Engineering, Mysore	Electronics and Communication Engineering	2003
d.	M. Tech.	Anna university, Chennai	Thanthai Periyar Government Institute of Technology, Vellore	Applied Electronics	2009

04	Work Experience	:	Teaching	Research	Industry	Others	Total	
			3years 10 months				3years 10 months	
	Date of Joining & Years	:	08-03-2010					
05	Area of Specialization	:	Applied Electronics					
06	Subjects taught at Under Graduate Level / Post Graduate Level							
	Under Graduate Level							
	Theory	:	UG	DSP, Modern communications, Principles of Communication, Power Electronics, Radio and TV Engineering, Electronic Circuits, Digital Electronics, Computer Organization, Control Systems, Microwave techniques and Applications.				
		:	PG					
	Practical	:	Analog Integrated circuits Lab, Electronic Circuits labs, Microprocessor-8085, Digital Signal Processing					
Project Work	:	3						

07	Research Guidance Nos.	No. of Papers Published in Master's / Ph.D Level	National Journals	International Journals	Conferences
		2			2

08	Projects Carried Out	:	
09	Patents	:	
10	Technology Transfer	:	
11	Research Publications	:	.
12	No. of Books Published with Details	:	

01	Name	:	SRIDEVI . S	
02	Date of Birth	:	22-07-1980	
03	Highest Educational Qualification	:	M.E(Communication Systems)	
04	Designation	:	Lecturer	

Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	X th STD	Matriculation	St.Joseph of Cluny Higher Secondary Mat.School	Biology,Maths,Phy.Science	1996
b.	XII th STD	Matriculation	St.Joseph of Cluny Higher Secondary Mat.School	Biology,Maths,Phy.Science	1998
c.	B.E	Periyar University	K.S.R College Of Technology	ECE	2002

05	Work Experience	:	Teaching	Research	Industry	Others	Total
			4.5	-	-	-	4.5
	Date of Joining	:	10-03-2010				
06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:	Electron Devices , Digital Electronics ,Digital Principal and system design, Microwave Engineering, Field Theory, Professional Ethics, Display system Engineering				
	Practicals	:	Digital Lab. Microprocessor Lab, Communication Lab,Electronics and Microprocessor Lab				
	Project Work	:					

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences
	-	-	-	-	1

08	Projects Carried Out (Nos.)	:	-
09	Patents	:	-
10	Technology Transfer	:	-
11	Research Publications	:	-
12	No. of Books Published with Details	:	-