



01	Name	:	K P Srinivasan					
02	Date of Birth	:	05-06-1948, 61					
03	Highest Educational Qualification	:	M.E. (I.I.Sc)					
04	Designation	:	Prof. and Head, Dept of CSE					
Qualifications								
Sl. No	Qualification	Board/IIT/NIT/University/Others		Institute & Place of Study	Area of Specialization	Year of Passing		
a.	Bachelor of Engineering	University of Madras		PSG College of Techhology, Coimbatore	Ellectrical Engineering	1970		
b.	Master of Engineering	Indian Institute of Science		School of Automation, I.I.Sc., Bangalore	Computer Science	1983		
Work Experience								
05	Work Experience	:35 Years		Teaching	Research	Industry	Others	Total
				1	1	13	20	35
	Date of Joining	:09/09/09						
Subjects teaching at Under Graduate Level / Post Graduate Level								
Under Graduate Level								
06	Theory	:	Computer Concepts and C Programming, Constitution of India and Ethical Practices					
	Practical	:						
	Project Work	:						
Research Publications								
07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level			National Journals	International Journals	Conferences	
						1		
08	Projects Carried Out	:	5					
09	Patents	:						
10	Technology Transfer	:						
11	Research Publications	:	1					
12	No. of Books Published with Details	:						

01	Name	:	Maheshwari	
02	Date of Birth	:	28/05/1969, 42	
03	Highest Educational Qualification	:	M.S. (CSE)	
04	Designation	:	ASST. PROFESSOR	


Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	SSLC				
b.	PUC/10+2				
c.	UG				
d.	PG				

05	Work Experience	:	Teaching	Research	Industry	Others	Total
			11		03		14
	Date of Joining	:	09/03/2010				

06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:					
		:					
		:					
		:					
		:					
	Post Graduate Level						
	Theory	:					
		:					
:							
Practicals	:						
	:						
	:						
Project Work	:						
	:						

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences

08	Projects Carried Out	:	
09	Patents	:	
10	Technology Transfer	:	
11	Research Publications	:	
12	No. of Books Published with Details	:	

01	Name	:	SREEDEVI N.	
02	Date of Birth	:	25.05.1975, 36	
03	Highest Educational Qualification	:	M.TECH.	
04	Designation	:	ASST. PROFESSOR	


Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	SSLC	Board of Secondary Education, Karnataka	St. Joseph's Convent, KGF		1990
b.	PUC	Pre University Board	First Grade College, KGF		1992
c.	BE (CSE)	Bangalore University	SJCIT, Chikaballapur	Computer Science & Engineering	1998
d.	M.TECH (CSE)	JNTU	JNTU, Hyderabad	Computer Science & Engineering	2005

05	Work Experience	:	Teaching	Research	Industry	Others	Total
			09 Year	-	-	-	09 Year
	Date of Joining	:	01.08.2007				

06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:	Data Structures & Algorithms				
			Finite Automata & Formal Languages				
			Operating Systems				
			Computer Organization				
			Computer Networks				
			Logic Design				
			Unix & System Programming				
			Computer Graphics				
			Data Communication				
			Object Oriented Analysis & design using UML				
	System Software						
	Practicals	:	Algorithms Lab				
			Microprocessor Lab				
Computer Graphics Lab							
Networks Lab							
Computer Concepts & Programming Lab							

		National Journals			
07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	--	International Journals	Conferences
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08	Projects Carried Out	:	Simulation of Pipeline Architecture, Regenerator Placement Algorithms for Optical Networks
09	Patents	:	Nil
10	Technology Transfer	:	Nil
11	Research Publications	:	Nil
12	No. of Books Published with Details	:	Nil

01	Name	:	ALPHA VIJAYAN	
02	Date of Birth	:	25.07.1976, 35	
03	Highest Educational Qualification	:	M.E. (CSE)	
04	Designation	:	ASST. PROFESSOR	

Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
1.	SSLC	SSLC BOARD	Dc,PAUL ENGLISH MEDIUM SCHOOL	-	1992
2.	PUC / 10+2	MG UNIVERSITY	St.Theresa's College,Ernakulam	-	1994
3.	UG Degree B.E	CUSAT	College of Engineering.Chengannur	CSE	2000
4.	PG Degree M.E.	Anna University	Jerusalem College of Engineering	CSE	2006

05	Work Experience	:	Teaching	Research	Industry	Others	Total
			8.5	-	-	-	8.5
	Date of Joining	:	26.07.2008				


06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:	Operating Systems				
			Software Engineering				
			Object Oriented Programming Concepts				
			Computer Concepts & C Programming				
Practicals	:	Data Structures Lab					
		Computer Programming Lab					
		Algorithms Lab					

National Journals					
07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	--	International Journals	Conferences
	--	02	---	--	02

	1. "Enhanced Logical tree Reconfiguration for Reliable Multicast", 'WEBTOPS 06", National Conference on WebTechnology and Open Source by Jaya Engineering College at CLRI Auditorium, Chennai on Feb 9, 10 2006		2. "Secure and More Reliable Data Communication in Mobile Adhoc Network", National Conference at Kamaraj College of Engineering & Technology, Virudunagar, Tamil Nadu, Jan 2008		
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08	Projects Carried Out	:	Data networking
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09	Patents	:	--
10	Technology Transfer	:	--
11	Research Publications	:	--
12	No. of Books Published with Details	:	--

01	Name	:	SHARAVANA.K	
02	Date of Birth	:	06-05-1981, 30	
03	Highest Educational Qualification	:	BE, (M.Tech)	
04	Designation	:	Sr. Lecturer	

Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	SSLC	Karnataka Board	Seshadripuram Boys High School, Bangalore		1997
b.	Diploma	D.T.E, Karnataka Board	Nadgir Institute of Polytechnic	Computer Science	2000
c.	BE	VTU, Belgium	MSRIT, Bangalore	Computer Science	2004
D.	M.E.	Sathyabama, Chennai	Sathyabama, Chennai	Computer Science	2009


CEP – Continuing Programmes, D/OEP: Distant / Online Education Programmes. Please Delete the rows not applicable

05	Work Experience	:	Teaching	Research	Industry	Others	Total
			4.5	Nil	Nil	Nil	4.5
	Date of Joining	:	11-10-2004				

06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:	Computer Concepts & C Programming				
			Advanced Computer Architecture				
			OOPs with C++				
			Unix & Shell Programming				
			Analysis & Design of Algorithms				
			Computer Networks				
	Practicals	:	Computer Graphics				
			C Programming Lab				
			System Software Lab				
			OOPS Lab				
Project Work	:	Data Structures Lab					
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07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences
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08	Projects Carried Out	:	Single Sign on (Accessing Multiple Server) Hospital Management- MVJCE (R&H)		
09	Patents	:	--		
10	Technology Transfer	:	--		
11	Research Publications	:	--		
12	No. of Books Published with Details	:	--		

01	Name	:	SANJAY KUMAR	
02	Date of Birth	:	21-01-1973, 38	
03	Highest Educational Qualification	:	M.E (CSE)	
04	Designation	:	Lecturer	

Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
1.	SSLC	Bihar Board	Mahavir High School, Gaya		1987
2.	INTERMEDIATE	Bihar Intermediate Council	J.J. College, Gaya		1990
3.	A.M.I.E	Institutions Of Engineers (India)	Institutions Of Engineers (India)	CSE	2001
4.	M.E	Anna University	TCE, Madurai	CSE	2008

04	Work Experience	:	Teaching	Research	Industry	Others	Total
			2 Y	-	1 Y	3 Y	4 Y
	Date of Joining	:	27-01-2009				
06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:	Data Structure And Algorithms				
		:	Microprocessor				
		:	Theory Of Computation				
:		Logic Design					
Practicals	:	Logic Design Lab					
07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D. Level	National Journals	International Journals	Conferences		
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08	Projects Carried Out	:	Region Based Image Retrieval Framework
09	Patents	:	--
10	Technology Transfer	:	--
11	Research Publications	:	--
12	No. of Books Published with Details	:	--

01	Name	:	M.V. KESAVAN	
02	Date of Birth	:	28-04-1981, 29	
03	Highest Educational Qualification	:	M.E.	
04	Designation	:	LECTURER	

Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
1.	SSLC	State Board, Tamil Nadu	Govt High School, Athani, Tamil Nadu	Nil	1996
2.	DIPLOMA	DOTe, Tamil Nadu	Sakthi Institute of Tech, Sakthi Nagar, Tamil Nadu	EEE	1999
3.	B.E	Bharathiyar University, Coimbatore	Kongu Engg College, Erode, Tamil Nadu	EIE	2002
4.	M.E	Anna University, Chennai	Sona College of Technology, Salem, Tamil Nadu	CSE	2008


05	Work Experience	:	Teaching	Research	Industry	Others	Total
			4.6	-	2		6.6
	Date of Joining	:	27-01-2009				


Subjects teaching at Under Graduate Level / Post Graduate Level			
Under Graduate Level			
06	Theory	:	Electronics Devices And Circuits
			Basic Electrical and Electronics
			Electrical Machines
			Electrical Estimation
Practicals		:	Electrical and Electronics Lab
			Electronics Devices and Circuits Lab

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D. Level	National Journals	International Journals	Conferences
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	Presented a paper titled "Association Rule Based Data Classification System" in the National Conference organized by Sona College of Technology, Salem, Feb 2008				
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08	Projects Carried Out	:	Association Rule Based Data Classification System
09	Patents	:	--
10	Technology Transfer	:	--
11	Research Publications	:	--
12	No. of Books Published with Details	:	--

01	Name	:	MOTHI BHAVANI				
02	Date of Birth	:	25.12.1983, 27				
03	Highest Educational Qualification	:	M.Tech.				
04	Designation	:	LECTURER				
Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing		
1.	SSLC	Board of Secondary Education, Karnataka	Sacred Heart Girls HS, Bangalore		1999		
2.	PUC	Pre University Board	Jothi Nivas College, Bangalore	PCME	2001		
3.	B.E. (CSE)	VTU	SCTIT, Bangalore	Computer Science & Engineering	2006		
4.	M.Tech.	VTU	MVJCE, Bangalore	Computer Science & Engineering	2008		
05	Work Experience	:	Teaching	Research	Industry	Others	Total
			2.9	-	-	-	2.9
	Date of Joining	:	04.09.2008				
Subjects teaching at Under Graduate Level / Post Graduate Level							
Under Graduate Level							
06	Theory	:	Computer Networks				
		:	Operating Systems				
		:	Finite Automata & Formal Language				
	Practical	:	Electronics Circuits & Logic Design Lab				
07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences		
	--	01	--	--	01		
Papers Presented:			Artificial Intelligence, RNSIT, 2007, National Conference				
08	Projects Carried Out	:	1. Image Processing / RRSSC, ISRO, Bangalore 2. Monitoring of turbines, GE, Bangalore				
09	Patents	:	Nil				
10	Technology Transfer	:	Nil				
11	Research Publications	:	Nil				
12	No. of Books Published with Details	:	Nil				

01	Name	:	Avayambal K	
02	Date of Birth	:	08.06.1980, 29	
03	Educational Qualifications	:	M.Tech (CNE)	
	Designation	:	Lecturer	

Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	SSLC	State board of TamilNadu	N.Kittappa Municipal HSS, Nagappatinam Dist		1995
b.	PUC/10+2	State board of TamilNadu	N.Kittappa Municipal HSS, Nagappatinam Dist	Maths , Physics, Chemistry, Biology	1997
c.	BE	Bharathidasan University, Trichy	AVC Engineering College,Nagappatinam Dist	Computer Science &Engineering	2001
d.	M.Tech	VTU,Belgaum	AMC Engineering College,Bangalore	Computer Network Engineering	2010


04	Work Experience	:	Teaching	Research	Industry	Others	Total
			06Y2M	Nil	Nil	Nil	06Y2M
	Date of Joining	:	20-07-2009				

05	Area of Specialization	:	Computer Network Engineering				
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06	Subjects teaching at Under Graduate Level / Post Graduate Level					
	Under Graduate Level					
	Theory	Computer networks				
		Web Technology				
		File Structures				
		Operating Systems				
		Distributed Operating System				
		OOPS through C++				
		Cryptography and Network Security				
		OOAD using UML				
		Finite Automata and Finite Languages				
		Computer Concepts and C Programming				
	Data Structures					
	Practicals	:	Data Structures Lab			
		:	C Programming Lab			
:		C++ programming Lab				
:		Computer Networks Lab				
:		File Structures Lab				
:		Operating System Lab				
Project Work		:	Guided Numerous Projects at UG Level			

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences
	--	--	--	--	2


08	Projects Carried Out	:	--
09	Patents	:	--
10	Technology Transfer	:	--
11	Research Publications	:	--
12	No. of Books Published with Details	:	--
13	Membership of Professional Bodies	:	--

01	Name	:	SUDHA. B	
02	Date of Birth	:	15-07-1979, 30	
03	Educational Qualifications	:	M.E. (CSE)	
	Designation	:	Lecturer	

Sl. No	Qualification	Board /IIT/NIT/ University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	SSLC	Matriculation Board, TamilNadu	Alvernia Matriculation Higher Secondary School	--	1994
b.	PUC/10+2	Matriculation Board, TamilNadu	Alvernia Matriculation Higher Secondary School	Maths , Physics, Chemistry, Biology	1996
c.	BE	Bharathidasan University, Trichy	Periyar Maniammai College of Tech. for Women, Vallam, Thanjavur	Civil Engineering	2000
d.	M.E	Anna University	College of Engineering, Guindy	Computer Science & Engineering	2002

CEP – Continuing Programmes, D/OEP: Distant / Online Education Programmes. Please Delete the rows not applicable


04	Work Experience	:	Teaching	Research	Industry	Others	Total
			03Y01M	Nil	01Y05M	Nil	04Y06M
	Date of Joining	:	27-07-2009				
05	Area of Specialization	:	Computer Science & Engineering				
06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	Software Engineering					
		Database Management Systems					
		Advances in Databases					
		Operating Systems					
		Software Testing					
		Advanced Computer Architecture					
		Artificial Intelligence C & Data Structures					
	Practicals	: Database Management Systems Lab					
: Operating System Lab							
: Data Structures Lab							
Project Work	:	Guided Numerous Projects at UG & PG Levels					
07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences		
	--	--	--	--	3		
08	Projects Carried Out	:	Hindsight – Software Product Testing during my tenure in IT industry				
09	Patents	:	--				
10	Technology Transfer	:	--				
11	Research Publications	:	--				
12	No. of Books Published with Details	:	--				
13	Membership of Professional Bodies	:	--				

01	Name	:	V.R.POORNA	
02	Date of Birth	:	20.05.1983, 27	
03	Educational Qualifications	:	M.E.(CSE)	
	Designation	:	Lecturer	

Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	SSLC	State board of Kreala	St.Therese's convent,Neyyattinkara	--	1998
b.	PDC	University of Kerala	H.H.M.S.P.B N.S.S college for women	Maths , Physics, Chemistry, Biology	2000
c.	BE(CSE)	M.S University,Tamilnadu	St.Xaviers Catholic College for women,Nagercoil	Computer Science & Engineering	2004
d.	M.E.(CSE)	Anna University,Tamilnadu	National Engineering College,Kovilpatti	Computer Science & Engineering	2006

CEP – Continuing Programmes, D/OEP: Distant / Online Education Programmes. Please Delete the rows not applicable


04	Work Experience	:	Teaching	Research	Industry	Others	Total
			03Y0M	Nil	Nil	Nil	03Y0M
	Date of Joining	:	03-08-2009				
05	Area of Specialization	:	Computer Networking				
06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory			Computer networks			
				Web Technology			
				File Structures			
				Operating Systems			
				System Software			
				OOPS through C++			
				Cryptography and Network Security			
				Artificial Intelligence			
	Practicals		:	Compiler Design			
				Computer Concepts and C Programming			
				Data Structures			
				Data Structures Lab			
				C Programming Lab			
				System Software Lab			
				Computer Networks Lab			
		:	Compiler Design Lab				
			Operating System Lab				
			Analysis and Design of Algorithms Lab				
	Project Work	:	Guided Numerous Projects at UG Level				
07	Research Guidance Nos.		No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences	
	--		---	--	--	2	
08	Projects Carried Out	:	--				
09	Patents	:	--				
10	Technology Transfer	:	--				
11	Research Publications	:	--				
12	No. of Books Published with Details	:	--				
13	Membership of Professional Bodies	:	--				

01	Name	:	Y.BHARATHI	
02	Date of Birth	:	23.08.1986, 23	
03	Educational Qualifications	:	M.TECH. (CNE))	
	Designation	:	Lecturer	

Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	SSC	Board of Secondary Education	Priyadarshini Residential High School,Pithapuram	--	2001
b.	Intermediate	Board of Intermediate	Pragathi Junior college,Kakinada	Maths , Physics, Chemistry	2003
c.	B.TECH(CSE)	JNTU,Andhra Pradesh	Sri Prakash College of Engineering,Tuni	Computer Science & Engineering	2007
d.	M.E.(CSE)	VTU	(Pursuing)AMC Engineering College,Bangalore	Computer Network Engineering	2009

CEP – Continuing Programmes, D/OEP: Distant / Online Education Programmes. Please Delete the rows not applicable

04	Work Experience	:	Teaching	Research	Industry	Others	Total
			00Y0M	Nil	Nil	Nil	00Y0M
	Date of Joining	:	03-08-2009				
05	Area of Specialization	:					
06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	Computer networks					
		Web Technology					
		File Structures					
		Operating Systems					
		System Software					
		OOPS through C++					
		Cryptography and Network Security					
		Artificial Intelligence					
		Compiler Design					
		Computer Concepts and C Programming					
	Data Structures						
	Practicals	:	Data Structures Lab				
			C Programming Lab				
			System Software Lab				
			Computer Networks Lab				
		Compiler Design Lab					
		Operating System Lab					
Project Work		:	Guided Numerous Projects at UG Level				
07	Research Guidance Nos.		No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences	
	--		2		--	2	
08	Projects Carried Out	:	1				
09	Patents	:	--				
10	Technology Transfer	:	--				
11	Research Publications	:	--				
12	No. of Books Published with Details	:	--				
13	Membership of Professional Bodies	:	--				

01	Name	:	SIVAGAMA SUNDARI G	
02	Date of Birth	:	05/06/1976, 34	
03	Highest Educational Qualification	:	B.E. (CSE)	
04	Designation	:	Lecturer	

Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	B.E.	UNIVERSITY of MADRAS	Adhiparsakthi Engineering College , Malemaruvathur.	Computer Science & Engineering	1998
b.					
c.					
d.					


CEP – Continuing Programmes, D/OEP: Distant / Online Education Programmes. Please Delete the rows not Applicable

05	Work Experience	:	Teaching	Research	Industry	Others	Total
			4	--	--	--	3
	Date of Joining	:	28/08/2006				

06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:	Design & Analysis of Algorithms				
			Computer Programming				
			System Modelling & Simulation				
			System Software				
	Practicals	:	Computer Programming lab				
			Systems Software lab				
			OOPS Lab				
			C Lab				

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	International Journals	Conferences
	NA	NA	NA	NA

08	Projects Carried out	:	Voice Recognition Sytems, Customer service processing, Correspondence Cell Education
09	Patents	:	Nil
10	Technology Transfer	:	Nil
11	Research Publications	:	Nil
12	No. of Books Published with Details	:	Nil

01	Name	:	MUTHAIAH U	
02	Date of Birth	:	16-07-1983, 27	
03	Highest Educational Qualification	:	B.Tech (IT)	
04	Designation	:	Lecturer	


Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	SSLC	Tamil nadu State Board	VNT Govt. HSS,S.pattianm		1999
b.	PUC	Tamil nadu State Board	St.arulanandar HSS, Oriyur		2001
c.	BTECH (IT)	Anna University	Mepco schlenk Engg. College,Sivakasi	IT	2005
d.	M.TECH (CSE)	---	----	-----	-----

05	Work Experience	:	Teaching	Research	Industry	Others	Total
			2 Y	-	-	-	2 Y
	Date of Joining	:	06-09-2007				

06	Subjects teaching at Under Graduate Level / Post Graduate Level		
	Under Graduate Level		
	Theory	:	Data Structures & Algorithms
			Finite Automata & Formal Languages
			Operating Systems
			Computer Organization
			Computer Networks
			Logic Design
			Unix & System Programming
			Computer Graphics
			Data Communication
			Object Oriented Analysis & design using UML
		System Software	
	Practicals	:	Algorithms Lab
			Microprocessor Lab
		Computer Graphics Lab	
		Networks Lab	
		Computer Concepts & Programming Lab	

National Journals					
07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	--	International Journals	Conferences
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08	Projects Carried Out	:	Video Steganography,Rota software,Mark analysis system
09	Patents	:	Nil
10	Technology Transfer	:	Nil
11	Research Publications	:	Nil
12	No. of Books Published with Details	:	--


01	Name	:	UMESH KUMAR	
02	Date of Birth	:	02/02/1982, 29	
03	Highest Educational Qualification	:	B.Tech (IT)	
04	Designation	:	Lecturer	

Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	BE	UP Technical University	Saroj Institute of Technology & management, Lucknow	Information Technology	2005

05	Work Experience	:	Teaching	Research	Industry	Others	Total
			2	Nil	1	Nil	3
	Date of Joining	:	15-02-2007				
06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:					
		:	C# & .Net Concepts				
		:	Computer concepts and C				
		:	Software Engineering				
		:	Engineering Technology & Management				
	Practicals	:					
		:	Computer concepts and C Lab				
		:	Analog & Digital Lab				
:							
:							

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences
	NA	NA	NA	NA	NA

08	Projects Carried Out	:	Financial Accounting of Vidhan Sabha Sachivalaya, Lucknow
09	Patents	:	-
10	Technology Transfer	:	-
11	Research Publications	:	-
12	No. of Books Published with Details	:	-

01	Name	:	Akanchha Mishra	
02	Date of Birth	:	07/11/1981, 28	
03	Highest Educational Qualification	:	M.Tech. (CSE)	
04	Designation	:	Lecturer	


Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	SSLC				
b.	PUC/10+2				
c.	UG				
d.	PG				

05	Work Experience	:	Teaching	Research	Industry	Others	Total
	Date of Joining	:	02/15/2010				

06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:					
		:					
		:					
		:					
		:					
	Post Graduate Level						
	Theory	:					
		:					
Practicals	:						
	:						
	:						
Project Work	:						

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences

08	Projects Carried Out	:	
09	Patents	:	
10	Technology Transfer	:	
11	Research Publications	:	
12	No. of Books Published with Details	:	

01	Name	:	Supriya Kamthania	
02	Date of Birth	:	14/08/1984, 25	
03	Highest Educational Qualification	:	M.Tech.	
04	Designation	:	Lecturer	


Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	SSLC				
b.	PUC/10+2				
c.	UG				
d.	PG				

05	Work Experience	:	Teaching	Research	Industry	Others	Total
	Date of Joining	:	03/04/2010				

06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:					
		:					
		:					
		:					
		:					
	Post Graduate Level						
	Theory	:					
		:					
:							
Practicals	:						
	:						
	:						
Project Work	:						
	:						

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences

08	Projects Carried Out	:	
09	Patents	:	
10	Technology Transfer	:	
11	Research Publications	:	
12	No. of Books Published with Details	:	

01	Name	:	Sanchari Saha	
02	Date of Birth	:	19/09/1985, 24	
03	Highest Educational Qualification	:	M.Tech.	
04	Designation	:	Lecturer	


Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	SSLC				
b.	PUC/10+2				
c.	UG				
d.	PG				

05	Work Experience	:	Teaching	Research	Industry	Others	Total
	Date of Joining	:	28/01/2008				

06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:					
		:					
		:					
		:					
		:					
	Post Graduate Level						
	Theory	:					
		:					
Practicals	:						
	:						
Project Work	:						

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences

08	Projects Carried Out	:	
09	Patents	:	
10	Technology Transfer	:	
11	Research Publications	:	
12	No. of Books Published with Details	:	

01	Name	:	Thangamani	
02	Date of Birth	:	26/08/1978, 31	
03	Highest Educational Qualification	:	M.Tech.	
04	Designation	:	Lecturer	


Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	SSLC				
b.	PUC/10+2				
c.	UG				
d.	PG				

05	Work Experience	:	Teaching	Research	Industry	Others	Total
	Date of Joining	:	22/08/2007				

06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:					
		:					
		:					
		:					
		:					
	Post Graduate Level						
	Theory	:					
		:					
Practicals	:						
	:						
Project Work	:						

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences

08	Projects Carried Out	:	
09	Patents	:	
10	Technology Transfer	:	
11	Research Publications	:	
12	No. of Books Published with Details	:	


01	Name	:	Somesekhar	
02	Date of Birth	:	15/08/1984, 25	
03	Highest Educational Qualification	:	M.Tech.	
04	Designation	:	Lecturer	

Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	SSLC				
b.	PUC/10+2				
c.	UG				
d.	PG				

05	Work Experience	:	Teaching	Research	Industry	Others	Total
	Date of Joining	:	08/03/2010				
06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:					
		:					
		:					
		:					
		:					
	Post Graduate Level						
	Theory	:					
		:					
:							
Practicals	:						
	:						
Project Work	:						
	:						

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences

08	Projects Carried Out	:	
09	Patents	:	
10	Technology Transfer	:	
11	Research Publications	:	
12	No. of Books Published with Details	:	

01	Name	:	Srikanth S.P	
02	Date of Birth	:	10/11/1975, 34	
03	Highest Educational Qualification	:	M.Tech.	
04	Designation	:	Sr. Lecturer	

Sl. No	Qualification	Board/IIT/NIT/University/Others	Institute & Place of Study	Area of Specialization	Year of Passing
a.	SSLC				
b.	PUC/10+2				
c.	UG				
d.	PG				

05	Work Experience	:	Teaching	Research	Industry	Others	Total
	Date of Joining	:	10/03/2010				

06	Subjects teaching at Under Graduate Level / Post Graduate Level						
	Under Graduate Level						
	Theory	:					
		:					
		:					
		:					
		:					
	Post Graduate Level						
	Theory	:					
		:					
:							
Practicals	:						
	:						
	:						
Project Work	:						
	:						

07	Research Guidance Nos.	No. of Papers Published in Master's/Ph.D Level	National Journals	International Journals	Conferences

08	Projects Carried Out	:	
09	Patents	:	
10	Technology Transfer	:	
11	Research Publications	:	
12	No. of Books Published with Details	:	